

10. (amended) A device as claimed in claim 1, wherein, within an encapsulation for the device, the main transistor section and the sense transistor section are provided as a first integrated circuit chip, and wherein the control means and the further control means are provided as at least one second integrated circuit chip.

REMARKS

The foregoing amendments to the claims were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights he may have under the Doctrine of Equivalents. Applicant furthermore reserves his right to reintroduce subject matter deleted herein at a later time during the prosecution of this application or continuing applications.

Respectfully submitted,

By 

Michael E. Marion, Reg. 32,266
Attorney
(914) 333-9641

APPENDIX

4. (amended) A device as claimed in claim 2 ~~or claim 3~~, wherein the adjustment circuit means includes an operational amplifier, first and second equal value resistors and a transistor, wherein a non-inverting input of this operational amplifier is connected to the voltage across the current sensing resistance, wherein one end of said first resistor is connected to the second main terminal of the device and the other end of said first resistor is connected to an inverting input of this operational amplifier and to a first main electrode of this transistor, wherein one end of said second resistor is connected to the first control signal voltage and the other end of said second resistor is connected to a second main electrode of this transistor which provides the second control signal, and wherein the output of this operational amplifier is connected to a control electrode of this transistor.

7. (amended) A device as claimed in claim 5 ~~or claim 6~~, wherein the comparison means includes an operational amplifier, wherein a non-inverting input of this operational amplifier is connected to the current limit defining reference voltage, wherein an inverting input of this operational amplifier is connected to the voltage across the current sensing resistance, and wherein the output of

this operational amplifier provides the first control signal.

8. (amended) A device as claimed in ~~any preceding claim~~claim 1, wherein the main transistor section comprises a power MOSFET or IGBT.

9. (amended) A device as claimed in ~~any one of claims 1 to 8~~claim 1, wherein, within an encapsulation for the device, the main transistor section, the sense transistor section, the control means and the further control means are provided as a single integrated circuit chip.

10. (amended) A device as claimed in ~~any one of claims 1 to 8~~claim 1, wherein, within an encapsulation for the device, the main transistor section and the sense transistor section are provided as a first integrated circuit chip, and wherein the control means and the further control means are provided as at least one second integrated circuit chip.